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# UTILITY PATENT APPLICATION TRANSMITTAL

(Only for new nonprovisional applications under 37 C.F.R. § 1.53(b))

Attorney Docket No. 476-1859

First Inventor or Application Identifier Anslow

Title High Density Printed Wiring Board Having In-Via Surface.....

Express Mail Label No. EL 350 752 054 US

## APPLICATION ELEMENTS

See MPEP chapter 600 concerning utility patent application contents.

1. ☒ \* Fee Transmittal Form (e.g., PTO/SB/17)  
(Submit an original and a duplicate for fee processing)
2. ☒ Specification [Total Pages 12]  
(preferred arrangement set forth below)
  - Descriptive title of the Invention
  - Cross References to Related Applications
  - Statement Regarding Fed sponsored R & D
  - Reference to Microfiche Appendix
  - Background of the Invention
  - Brief Summary of the Invention
  - Brief Description of the Drawings (if filed)
  - Detailed Description
  - Claim(s)
  - Abstract of the Disclosure
3. ☒ Drawing(s) (35 U.S.C. 113) [Total Sheets 6]
4. Oath or Declaration [Total Pages]
  - a. ☐ Newly executed (original or copy)
  - b. ☐ Copy from a prior application (37 C.F.R. § 1.63(d))  
(for continuation/divisional with Box 16 completed)
    - i. ☐ DELETION OF INVENTOR(S)  
Signed statement attached deleting inventor(s) named in the prior application, see 37 C.F.R. §§ 1.63(d)(2) and 1.33(b).

\* NOTE FOR ITEMS 1 & 13: IN ORDER TO BE ENTITLED TO PAY SMALL ENTITY FEES, A SMALL ENTITY STATEMENT IS REQUIRED (37 C.F.R. § 1.27), EXCEPT IF ONE FILED IN A PRIOR APPLICATION IS RELIED UPON (37 C.F.R. § 1.28).

## ADDRESS TO:

Assistant Commissioner for Patents  
Box Patent Application  
Washington, DC 20231

5. ☐ Microfiche Computer Program (Appendix)
6. Nucleotide and/or Amino Acid Sequence Submission  
(if applicable, all necessary)
  - a. ☐ Computer Readable Copy
  - b. ☐ Paper Copy (identical to computer copy)
  - c. ☐ Statement verifying identity of above copies

## ACCOMPANYING APPLICATION PARTS

7. ☐ Assignment Papers (cover sheet & document(s))
8. ☐ 37 C.F.R. § 3.73(b) Statement of Power of Attorney  
(when there is an assignee)
9. ☐ English Translation Document (if applicable)
10. ☒ Information Disclosure Statement (IDS)/PTO-1449 ☒ Copies of IDS Citations
11. ☐ Preliminary Amendment
12. ☒ Return Receipt Postcard (MPEP 503)  
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13. ☐ \* Small Entity Statement(s) ☐ Statement filed in prior application, Status still proper and desired  
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Prior application information: Examiner \_\_\_\_\_ Group / Art Unit: \_\_\_\_\_

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**Philip Andrew Anslow et al.**

**High Density Printed Wiring Board Having  
In-Via Surface Mounting Pads**

SERIAL NO: To be assigned  
FILING DATE: Herewith

1. Utility Patent Application Transmittal
2. Fee Transmittal for FY 1999 in duplicate
3. Check No. 36608 for \$760.00
4. Claim for Priority
5. Certified Copy of UK Registration No. 9828656.0
6. Specification with six (6) sheets of drawings
7. Form PTO-1449 with attached cited references
8. Certificate of EXPRESS MAIL.
9. Return Post Card.

DATE SENT: November 30, 1999

*"Express Mail" mailing label number*

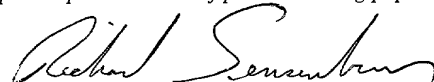
EL 350 752 054 US

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APPLICATION FOR LETTERS PATENT

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FOR

**HIGH DENSITY PRINTED WIRING BOARD HAVING  
IN-VIA SURFACE MOUNTING PADS**

## HIGH DENSITY PRINTED WIRING BOARD HAVING IN-VIA SURFACE MOUNTING PADS

### Field of the invention

5 The present invention relates to a high density printed wiring board having blind vias configured as surface mounting pads for surface mount electrical/electronic components, and to a method of manufacturing said wiring board.

### Background of the invention

10 It is known to employ blind vias in multi-layer wiring boards as a means of electrically connecting circuits formed in copper foil layers on opposite faces of laminae comprising the board. This can be better understood by reference to figures 5(a) to 5(g) which illustrate a method of forming a conventional printed wiring board having blind via electrical connects.

15 Figure 5(a) shows a two-sided printed wiring board lamina 10 before lamination with similar laminae to form a printed wiring board. The lamina 10 has a dielectric resin substrate 12 with copper foil layers (14,16) formed on opposite surfaces thereof. In a first process step for forming a conventional printed wiring board, a through hole 18 is drilled in the lamina 10, as illustrated in figure 5(b). As a second step (figure 5(c)), the through hole 18 is plated to form an electrical connect layer 20 extending between the surface copper foil layers (14,16) of the lamina 10. As illustrated in  
20 figure 5(c), the through hole plated layer 20 extends a short radial distance  $\ell$  over the surface copper foil layers (14,16) of the lamina 10 in the proximity of the through hole 18 and, as such, sits higher than the surface copper layers (14,16). Also as illustrated in figure 5(c), the lower (as shown in the drawing) surface foil

layer 16, which will become a buried inner layer of the printed wiring board when formed, is imaged and etched to form a electrical circuit 22 thereon.

Figure 5(d) illustrates how a conventional printed wiring board 24 can be formed from laminae 10 of identical or similar form to that illustrated in figure 5(c). The laminae 10 are placed together with a prepeg separation layer 26 inserted therebetween at the time of lamination in a conventional manner. The lamination process results in the innermost openings of the through holes 18 in the laminae 10 being covered thus constituting blind vias 28 which electrically connect circuits formed on the opposing surfaces of their respective lamina. The lamination process may also result in some prepeg material being forced into the holes 18 of the blind vias 28. After the lamination process, a through hole 30 to electrically connect the outer copper foil layers 14 and the buried inner circuit layers 22 is drilled in the printed wiring board and plated also in a conventional manner. The outer surface copper layers 14 of the printed wiring board 24 can then be imaged and etched to form outer electrical circuits 32. In particular, re-flow pads 34 may be formed for mounting electrical components on the surfaces 14 of the printed wiring board 24. As more readily seen in figure 6, the re-flow pads 34 are electrically connected to the through hole plated layers 20 by narrow conductor lines 36. A re-flow pad is used only for mounting a surface mount component and a narrow conductor line is used only for connecting this to a through hole plated layer of a blind via. Flat areas are necessary for mounting surface mount components. Consequently, it can be seen from figure 6 that the surface area occupied by a dog bone contact pad means consisting of a re-flow pad, a narrow conducting line and a blind via plated layer is considerably greater than the area occupied by only the blind via plating layer. A consequence of this is illustrated by figure 7 which is a side sectional view of an upper portion of a printed wiring board 24 on which electrical component 38 has been mounted. The contact elements 40 of the component 38 are attached to reflow pads 34 of dog bone contact pad means which are spaced inwardly from corresponding blind vias 28. It will be appreciated that the surface envelope E surrounding the component and the contact pad means mounting it on the board has a surface area substantially greater than that of the component itself.

The amount of surface area occupied by the dog bone contact pad means is particularly acute with respect to mounting electrical components have a ball grid array (BGA) surface contact arrangement. As illustrated in figure 8, the surface area (bounded by broken line E1) occupied by the contact pad means 42 is such that it dictates a minimum spacing between BGA contact pads (not shown). This would still be the case if only a small number of the BGA electrical connections are to be made through blind vias to other circuit layers of the printed wiring board.

Accordingly, the maximum mounting density for surface mount components and, in particular, surface mount components having a BGA mounting arrangement is relatively low.

Another conventional printed wiring board having blind holes formed by laser is described below with reference to figures 9(a) and 9(b).

More specifically, a printed wiring board 100 includes a dielectric resin substrate, 102, copper conductor pads (inner layer pattern) 104 and 109 on opposing surfaces of the substrate 102, dielectric resin layers 101 and 103 laminated on opposing surfaces of the substrate 102, outer layer copper reflow pads 108 and 113, and outer layer copper conductor lines 105 and 110. Blind holes 106 and 111 which have copper plating layers 107 and 112 are formed in the dielectric resin layers 101 and 103. Plated through holes 114 and 115 are formed through the layers 101 and 103 and the substrate 102 and have through hole copper plating layers 116 and 117 for connecting outer layers with the inner pads 104 and 109. The reflow pads 108 and 113 which mount electric components of the surface are connected to the copper layers 107 and 112 of the blind holes 106 and 111 by narrow conductor lines 105 and 110. The inner layer conductor pads 104 and 109 are connected to the narrow conductor lines 105 and 110, and thus the outer layers 108 and 113, by the copper plating layers 107 and 112 of the blind holes 106 and 111.

Figures 10(a) to 10(g) illustrate a conventional printed wiring board manufacturing process which uses a laser beam.

In the first step of the process, the inner layer conductor pads 104 and 109 are etched and formed on both surfaces of the dielectric resin substrate 102 as shown in figure 10(a). In the second step of the process, the dielectric resin layers 101

and 103, and copper foil layers (outer layers) 105a and 110a are laminated and pressed onto the substrate as etched and formed in the first step shown in figure 10(a), in a heated environment. In the third step of the process, special windows 118 and 119, through which a laser beam can be radiated, are etched as shown in figure 10(c). In the fourth step of the process, laser beams 120 are radiated through the windows 118 and 119 to form blind holes 106 and 111 which reach the inner layer conductor pads 104 and 109, as shown in figure 10(d). In the fifth step of the process, through holes 114 and 115 are drilled as shown in figure 10(e). In the sixth step of the process, blind holes 106 and 111, and through holes 114 and 115 are plated with plating layers 107, 112, 116 and 117 as shown in figure 10(f) for connecting the copper foil layers 105a and 110a with the inner layer pads 104 and 109. In the last step of the process, the copper foil layers 105a and 110a are etched to form narrow outer layer conductor lines 105 and 110 and reflow pads 108 and 113, as shown in figure 10(g).

However, as for the first conventional printed wiring board as described above with respect to figures 5 to 8, on the second conventional printed wiring board 100, the reflow pads 108 and 113 are used only for mounting surface mount components, and the narrow conductor lines 105 and 110 are used only for connecting to the blind holes 106 and 111, respectively, because the conductor pad means around the blind holes 106 and 111 are higher than the outer layer copper areas and flat surfaces are necessary for mounting the surface mount components.

Accordingly, the maximum mounting density for surface mounted components on the second conventional printed wiring board as abovedescribed is relatively low.

In the case of the conventional printed wiring board formed by the process illustrated by figures 10(a) to 10(g), US 5,315,072 discloses a method to overcome the problem of the contact pads means being higher than the outer layer copper areas around the blind holes. The method involves forming in separate laminae blind holes which, when the laminae are placed (laminated) together, are directed inwardly of the reflow pads which extend over the blind holes. In this way, the surface area occupied by the contact pad means is reduced when compared to the conventional arrangements. However, the process for forming a printed wiring

board in accordance with US 5,315,072 involves a number of additional process steps over the conventional process illustrated by figures 10(a) to 10(g).

### **Object of the invention**

5 It is an object of the present invention to provide a printed wiring board and a method of forming the same that obviates disadvantages associated with conventional printed wiring boards and methods of manufacturing such boards.

10 It is another object of the present invention to provide a method of manufacturing a printed wiring board having in-via surface mount pads and a printed wiring board manufactured by the method.

It is a further object of the present invention to provide a printed wiring board having a high mounting density for surface mount components.

### **Summary of the invention**

15 According to a first aspect of the present invention, there is provided a printed wiring board, comprising: a plurality of spaced a part circuit layers, at least two of said layers being electrically connected by a blind hole/via, wherein a surface contact pad is formed by depositing over said blind hole/via an electrically conductive metallic layer.

20 According to a second aspect of the present invention, there is provided a method of manufacturing a printed wiring board, comprising the steps of:- providing a printed wiring board with a plurality of spaced a part conductor layers, at least two of said layers being electrically connected by a blind hole/via; and forming a surface contact pad by depositing an electroconductive metallic layer over said blind  
25 hole/via.



**Brief description of the drawings**

For a better understanding of the invention and to show how the same may be carried into effect, there will now be described by way of example only, embodiments according to the present invention with reference to accompanying drawings in which:

5

Figure 1 is a side sectional view in schematic form of a first embodiment of a printed wiring board;

Figure 2 is a side sectional view of an upper portion of the embodiment of figure 1 showing a component mounted thereon;

10

Figure 3 is a plan view of a ball grid array (BGA) contact pad means arrangement for the first embodiment;

Figure 4 is a side sectional view in schematic form of a second embodiment of a printed wiring board in accordance with the invention;

15

Figures 5(a) to 5(d) are sectional views in schematic form showing the manufacturing steps involved in the method of manufacturing a first conventional printed wiring board;

Figure 6 is a plan view of a contact pad means for the first conventional printed wiring board;

20

Figure 7 is a side sectional view of an upper portion of the first conventional printed wiring board showing a component mounted thereon;

Figure 8 is a plan view of a BGA contact pad means arrangement for the first conventional printed wiring board;

Figure 9(a) is plan view of a contact pad means for a second conventional printed wiring board;

25

Figure 9(b) is a side sectional view of schematic form of a second conventional printed wiring board; and

Figures 10(a) to 10(g) are side sectional views in schematic form showing the manufacturing steps involved in the method of manufacturing the second conventional printed wiring board.

5      **Detailed description of the preferred embodiments**

There will now be described by way of example preferred embodiments of the present invention. In the following description, numerous specific details are set forth in order to provide a thorough understanding of the present invention. It will be apparent, however, to one skilled in the art, that the present invention may be practised without limitation to the specific details. In other instances, well known methods and structures have not been described in detail so as not to unnecessarily obscure the present invention.

Referring to figure 1, it can be seen that a first embodiment of a printed wiring board in accordance with the present invention has a structure generally similar to that of the first conventional printed wiring board in figure 5(d). In the following description, like numerals to those used in connection with the first conventional printed wiring board will be employed but prefixed by a "2".

The structure of the printed wiring board 224 in accordance with a first embodiment of the invention differs from that of the first conventional printed wiring board in that reflow pads 250 for mounting surface mount components to the board 224 are formed to be coincident with the blind vias 228. As such, there is no requirement for narrow conductor lines to connect the reflow pad to the blind via-plated layers 220. As a consequence, contact pad means of the first embodiment occupy a board surface area no greater than the surface area of a plated layer of a blind via itself.

As illustrated in figure 2 which shows a surface mount component 238 attached to contact pad means of the printed wiring board, it can be seen that the surface area occupied by the surface envelope E enclosing the component and contact pad means is, in fact, the surface area occupied by the component itself since the contact pad means lie within the envelope E of the component. It will be appreciated that with such an arrangement it is possible to mount components on

the board more closely together thus increasing the component mounting density of the board.

Figure 3 is a plan view of a ball grid array (BGA) contact pad means arrangement achievable with the printed wiring board according to the first embodiment of the invention. Since the contact pad means are not of the conventional dog bone form, it is possible to more closely place the contact pad means together, as shown in the left half of the drawings, where the component structure permits. In the case where the spacing between contact pads (balls) of the component having a BGA mounting arrangement cannot be brought closer together due to physical and operating requirements of the component, as shown in the right hand half of the drawings, it is possible to use the space between the rows of BGA contact pad means on the printed board to route connector tracks 260 for other components. In the conventional boards as aforescribed, the dog bone arrangement of the contact pad means and the limited spacing between such contact pad means for a BGA arrangement are such that it is not possible to utilise the space between the contact pad means for routing connector tracks. In the present invention, the availability of this space for routing connector tracks allows the wiring density on the board to be greatly increased as will be apparent to a skilled man.

In a method of manufacturing a printed wiring board having the structure of the first embodiment of the invention, it will be appreciated that the manufacturing process is generally similar to that described in connection with the first conventional printed wiring board as illustrated by figures 5(a) to 5(d). The process is modified, however, to include an additional step prior to plating the drilled through hole 230 (referenced as 30 in figure 5(d)). This additional step is to treat the surfaces of the board to prepare the outward facing surfaces of the plated layers of the blind vias to receive a copper capping layer. The process step comprises cleaning the outer surfaces of the board with a pumice scrub which also slightly roughens said surface but which does not abrade the blind via plated layers which sit higher than the remaining copper area of the surfaces. In fact, it is only necessary to treat those parts of the outer surfaces which are to be plated but in the case of pumice scrubbing, it is more convenient to treat the whole surface than to mask the surface and treat only those portions requiring such treatment. It will be appreciated by one skilled in the art that any other suitable process for preparing the portions of the

outer surfaces of the board which are to be plated can be employed. A further advantage of the pumice scrubbing step is that it removes any prepreg material which extends upwardly through the holes of the blind vias above the level of the plated layers of said vias.

- 5        Once the outer surfaces of the board are prepared, the through hole is plated in the conventional manner and simultaneous with this the blind vias are capped with a copper layer in the same process.

As a further preliminary step to the through hole plating step, it is preferable that the surface of the prepreg material in the blind via holes is treated to roughen it in preparation for the plating process. This can be achieved by applying a chemical etchant to the surface of the prepreg material. An agent such as permanganate desmear would be suitable for this process although one skilled in the art would recognise that any other suitable etchant can be employed.

10

It can be seen that the manufacturing process for providing printed wiring boards in accordance with a first embodiment of the invention requires only one additional step of treating the surfaces of the boards prior to plating the through hole (or through holes) and a modification of the through hole plating step to include depositing capping layers on the lined vias as required.

15

It will be appreciated that a printed wiring board providing the advantages as provided by the board according to the first embodiment of the invention can be formed with a combination of conventional contact pad means and in-via contact pad means as required.

20

A printed wiring board in accordance with a second embodiment of the invention has a structure generally similar to that of the second conventional printed wiring board illustrated in figure 9(b). In the following description, like numerals to those used in connection with the second conventional printed wiring board will be employed but prefixed by a "3" rather than a "1" as in figures 9 and 10. As with the first embodiment of the invention, the difference between the second embodiment and the conventional board illustrated in figure 9(b) is that contact pad means are formed by capping with a electrically conductive metallic material such as copper the blind holes (306,311) thus negating the need to have outwardly extending

25

30

reflow pads connected to the plated layers of the blind holes via a narrow conducting lines. The advantages provided by this embodiment of the invention are generally identical to those as described with respect to the first embodiment.

5 A manufacturing process for the printed wiring board in accordance with the second embodiment of the invention is generally similar to that as described with reference to figures 10(a) to 10(g) for the second conventional printed wiring board. In order to provide printed wiring boards in accordance with the second embodiment, it is possible modify the process for the second conventional wiring board in a number of ways. A first modification of the process is to proceed to the step of plating the  
10 through holes (311,315) and laser formed blind holes (306,311). Following this, it is necessary to fill the blind holes with an electro-conductive material such as an electro-conductive paste containing metallic particles, to prepare the outer surfaces of the board or at least the outwardly facing portions of the blind hole plated layers in preparation for a further plating step in which copper capping layers 350 are  
15 deposited over the blind holes.

As an alternative to the above, the process may comprise proceeding to the conventional process step of laser forming the blind holes, plating the blind holes, filling the blind holes with an electro-conductive filler material, drilling the through holes, preparing the surfaces of the board for the further plating process and then  
20 plating the through holes and simultaneously plating the blind holes with a copper capping layer.

It will be apparent to a man skilled in the art that it is possible to modify in other ways the conventional process for forming the second conventional printed wiring board in order to provide a process for forming the wiring board in accordance with  
25 the second embodiment of the invention.

**What is claimed is:**

1. A printed wiring board, comprising:

5 a plurality of spaced a part circuit layers, at least two of said layers being electrically connected by a blind hole/via wherein  
a surface contact pad is formed by depositing over said blind hole/via an electrically conductive metallic layer.

2. A method of manufacturing a printed wiring board, comprising the steps of:-

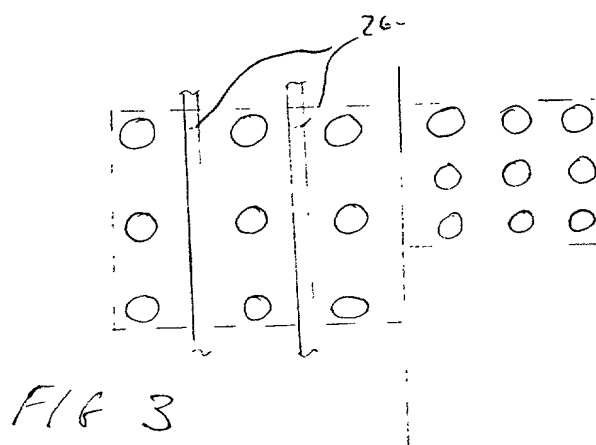
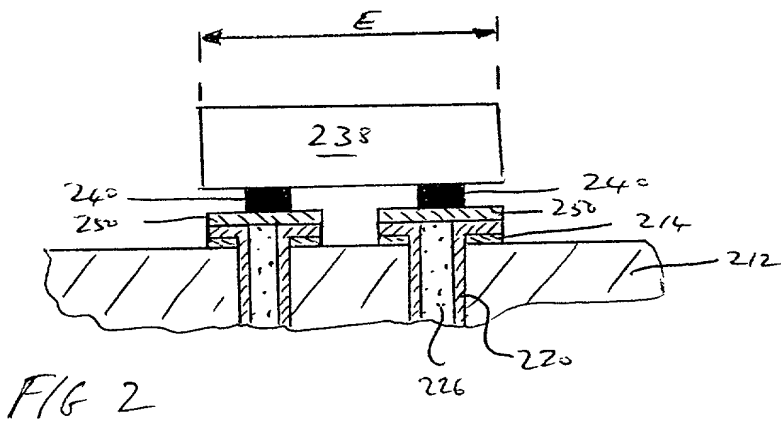
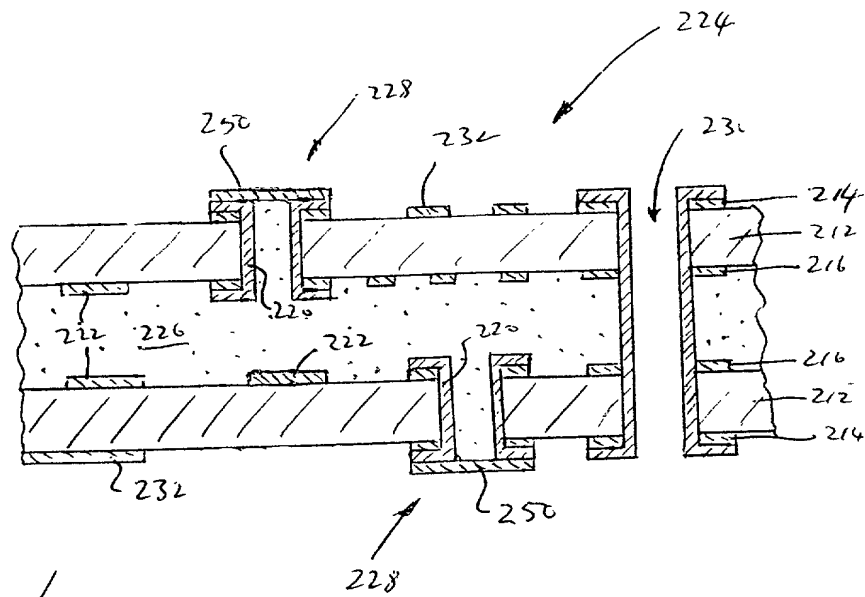
10 providing a printed wiring board with a plurality of spaced a part conductor layers, at least two of said layers being electrically connected by a blind hole/via; and  
forming a surface contact pad by depositing an electroconductive metallic layer over said blind hole/via.

15 3. A method as claimed in claim 2, wherein the step of forming the surface contact pad comprises plating an upper surface of said blind hole/via.

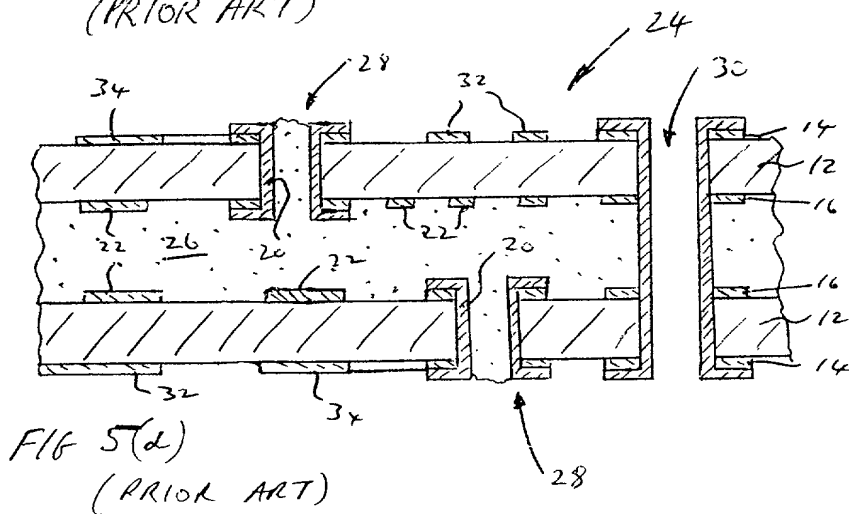
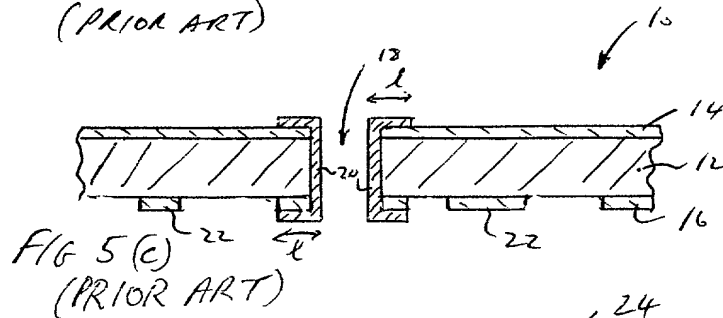
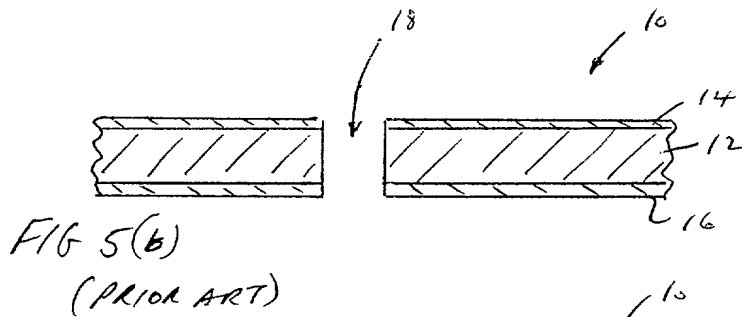
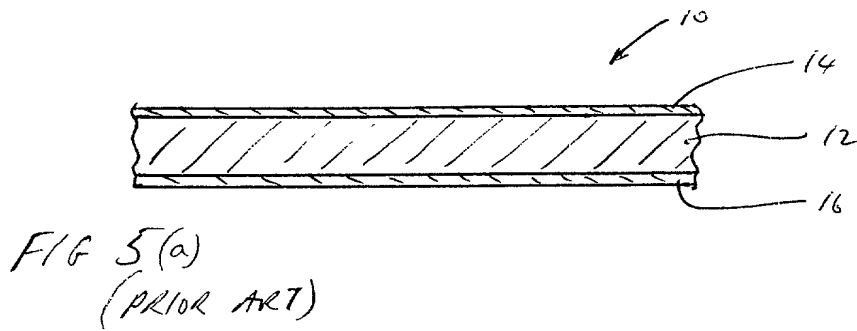
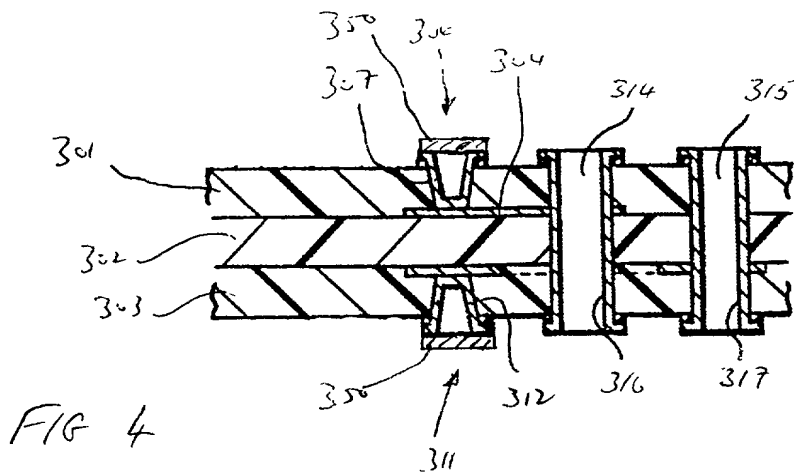
**HIGH DENSITY PRINTED WIRING BOARD HAVING IN-VIA  
SURFACE MOUNTING PADS**

**Abstract**

5 A multilayer printed wiring board in which surface contact pads are formed by  
depositing electrically conductive metallic layers directly over blind holes/vias. This  
allows ball grid array devices to be mounted on the printed wiring boards with a  
greater contact pad density than is the case where the contact pads are formed to  
the sides of the blind holes/vias. In a method of manufacturing printed wiring  
boards embodying the invention, it is desirable to treat the surfaces of the board to  
prepare the outward facing surfaces of the plated layers of the blind vias to receive  
10 copper capping layers.







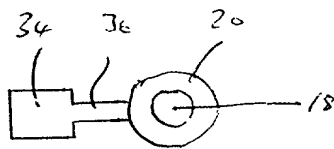


FIG 6  
(PRIOR ART)

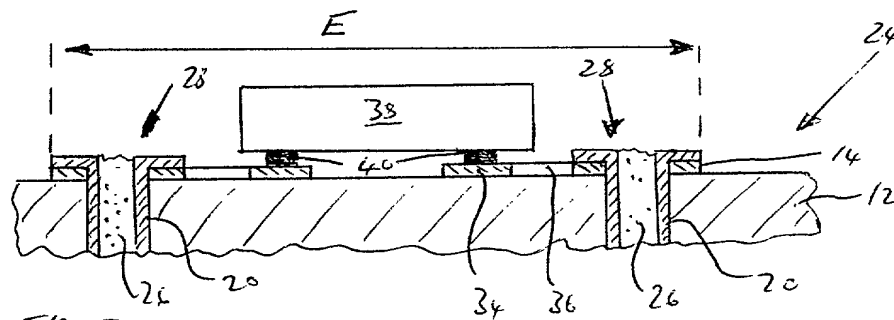


FIG 7  
(PRIOR ART)

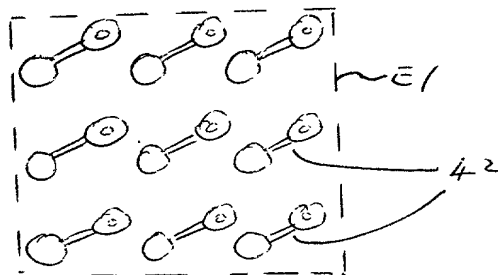


FIG 8  
(PRIOR ART)

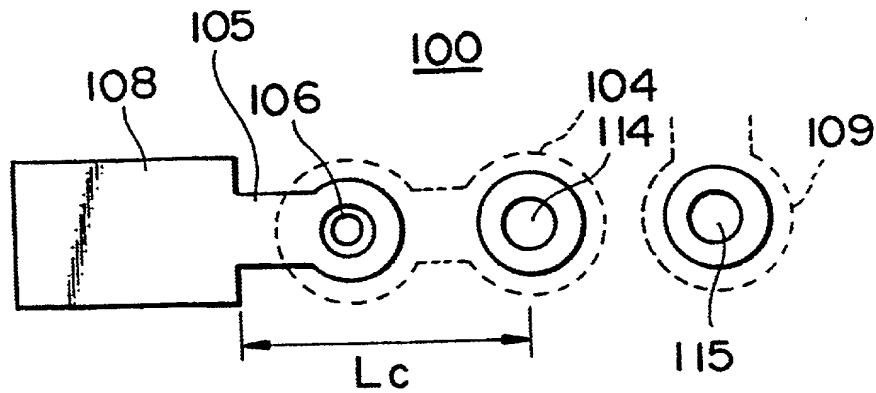


FIG 9(a) (PRIOR ART)

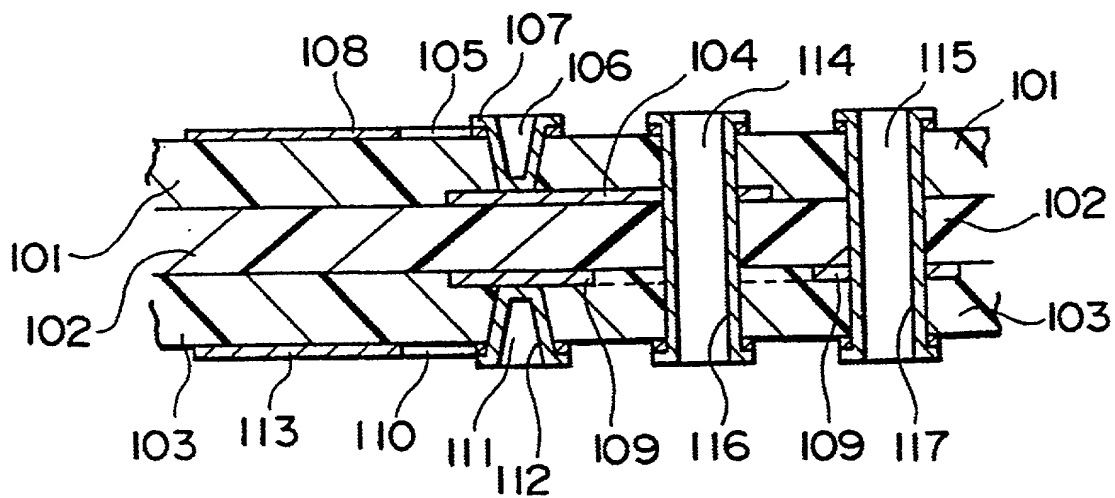


FIG 9(b) (PRIOR ART)

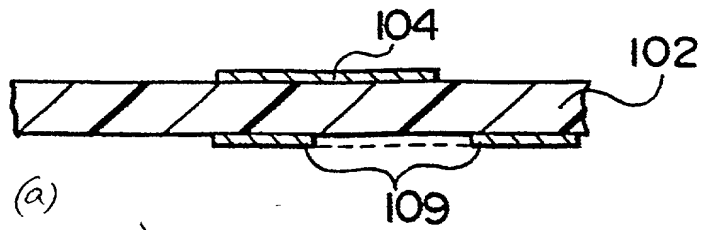


FIG 10 (a)  
(PRIOR ART)

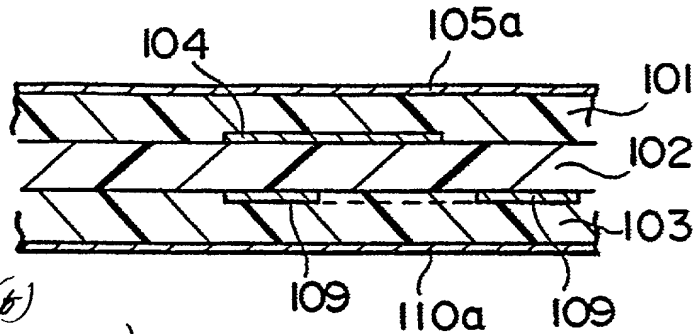


FIG 10(b)  
(PRIOR ART)

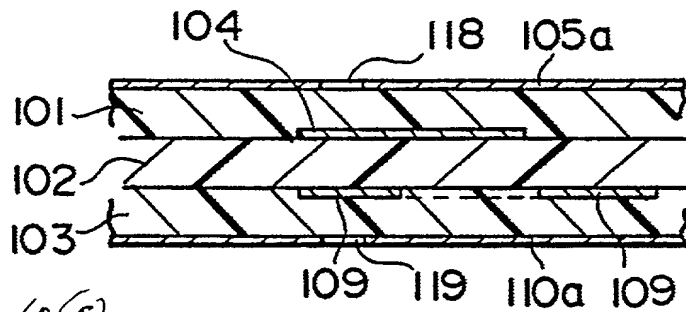


FIG 10(c)  
(PRIOR ART)

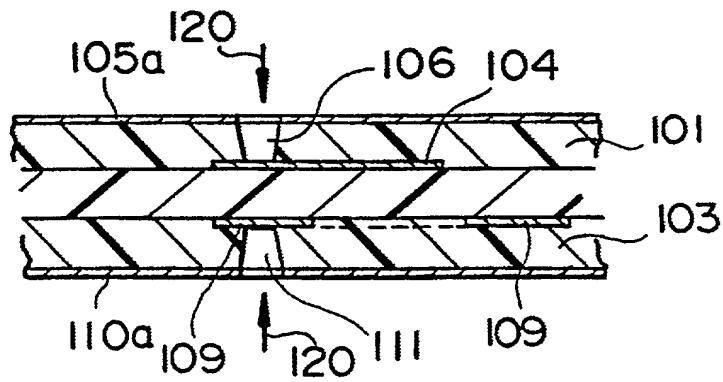


FIG 10(L)  
(PRIOR ART)

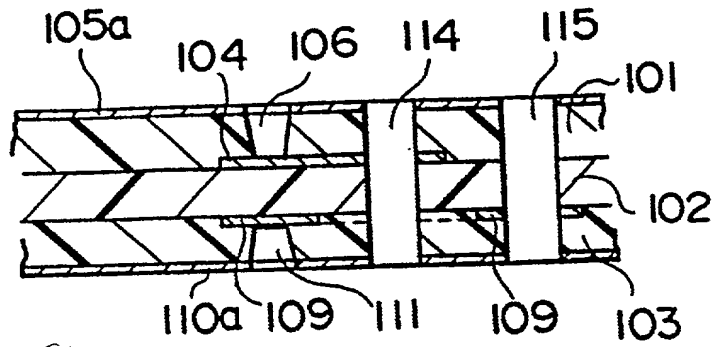


FIG 10(e)  
(PRIOR ART)

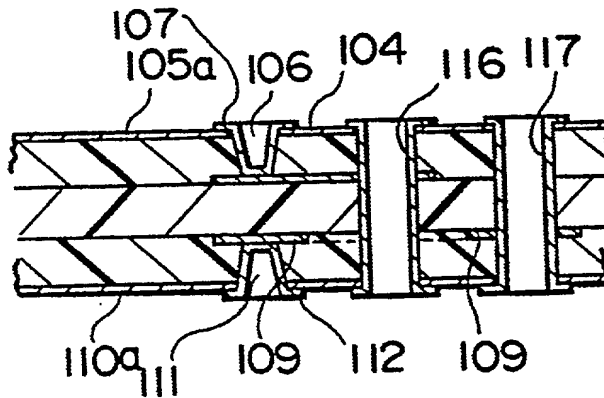


FIG 10(f)  
(PRIOR ART)

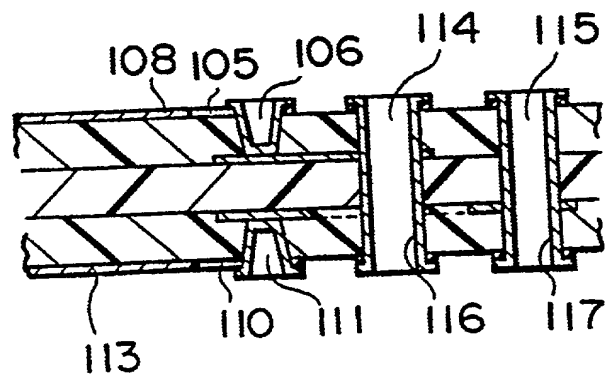


FIG 10(g)  
(PRIOR ART)